

DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Semiconductor Transistor Devices and Methods for Forming Semiconductor Transistor Devices, the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

POWER OF ATTORNEY:

As a named Inventor, I hereby appoint the following attorneys and agent to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Richard J. St. John, Reg. No. 19,363; David P. Roberts, Reg. No. 23,032; Randy A. Gregory, Reg. No. 30,386; Mark S. Matkin, Reg. No. 32,268; James L. Price, Reg.

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11 I hereby declare that all statements made herein of my own
12 knowledge are true and that all statements made on information and
13 belief are believed to be true; and further that these statements were
14 made with the knowledge that willful false statements and the like so
15 made are punishable by fine or imprisonment, or both, under
16 Section 1001 of Title 18 of the United States Code and that such willful
17 false statement may jeopardize the validity of the application or any
18 patent issued therefrom.

* * * * *

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* * * * *

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TELETYPE UNIT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Ahmad, Aftab ; Keller, David J.

Application No.: 09/167,174 Group No.: 2811
Filed: October 6, 1998 Examiner: O. Nadav
For: Semiconductor Transistor Devices and Methods for Forming Semiconductor Transistor
Devices.

Assistant Commissioner for Patents
Washington, D.C. 20231

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